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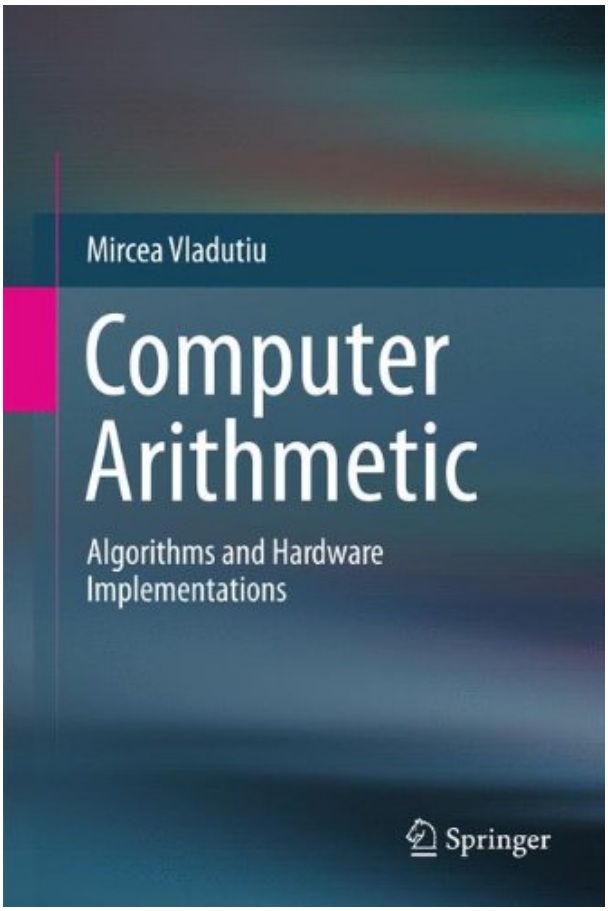

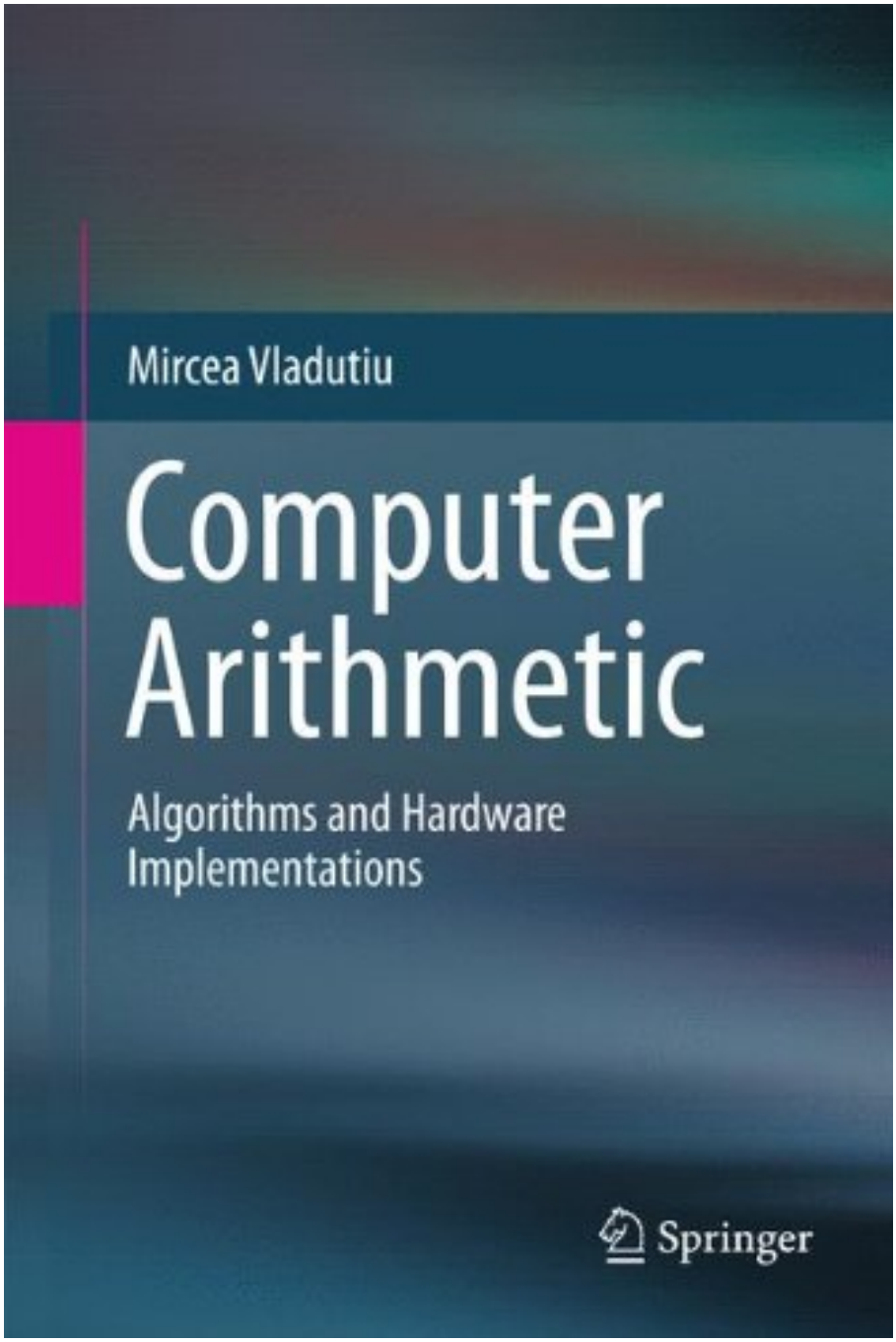

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Review

From the reviews:

“The objective of the book is to give a fundamental understanding of the principles of analysis and design of computer arithmetic devices. … The monograph is very well designed as a textbook for students and as a handbook for researchers and engineers of computer architecture and hardware.” (Telman Aliev, Zentralblatt MATH, Vol. 1255, 2013)

From the Back Cover

The subject of this book is the analysis and design of digital devices that implement computer arithmetic. The book's presentation of high-level detail, descriptions, formalisms and design principles means that it can support many research activities in this field, with an emphasis on bridging the gap between algorithm optimization and hardware implementation. The author provides a unified view linking the domains of digital design and arithmetic algorithms, based on original formalisms and hardware description languages.

A feature of the book is the large number of examples and the implementation details provided. While the author does not avoid high-level details, providing for example gate-level designs for all matrix/combinational arithmetic structures.

The book is suitable for researchers and students engaged with hardware design in computer science and engineering.

About the Author

Prof. Vladutiu has been a professor at the Computer Engineering Department of the “Politehnica” University

of Timisoara since 1990. He is founder and director of the ACSA Lab, as well as of the Department of Continuing Education, created at the “Politehnica” University of Timisoara in 1998. His main research interests are computer architectures, computer arithmetic, computer reliability and testing, quantum computing, and bio-inspired computing systems.tor for CONTI conferences organized by the “Politehnica” University of Timisoara.

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